Substitute for form 1449A/PTO

INFORMATION DISCLOSURE
STATEMENT BY APPLICANT

(use as many sheets as necessary)

Complete if Known	
Application Number	10/750,034
Filing Date	December 31, 2003
First Named Inventor	Suneel G. Mitbander
Art Unit	2131
Examiner Name	To Be Determined
Attorney Docket Number	42P18011

Sheet	1	of	1

NON PATENT LITERATURE DOCUMENTS				
Examiner Initials*	Cite No.¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.		T²
/JPT/		Ravi Budruk, et al. "PCI Express System Architecture", PC System Architecture Series, MindShare, Inc., September 2003 (Contents, pgs. vii-xxxvii; and Chapter 14 - Link Initialization & Training , pgs. 499-553)		

Examiner Signature	/John P Trimmings/	Date Considered	01/10/2008

*Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication.

¹Applicant's unique citation designation number. ²Applicant is to place a check mark here if English language Translation is attached.

Based on PTO/SB/08B (08-03) as modified by Blakely, Sokoloff, Taylor & Zafman (wlr) 08/11/2003.
Send To: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450

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/JPT/		PCT International Search Report (dated May 9, 2005), International Application No. PCT/US2004/043427 - International Filing Date 12/23/2004 (11 pgs.)		
/JPT/		Jay J. Nejedlo, "IBIST^{TM} (Interconnect Built-in-Self-Test) Architecture and Methodology for PCI Express", Institute of Electrical and Electronics Engineers, Proceedings International Test Conference 2003, Charlotte, NC, USA Sept. 30 - Oct. 2, 2003 (IEEE, US Vol. 2, 30 September 2003) XP010685400, ISBN: 0-7803-8106-8 (pages 114-122)		
/JPT/		Jay J. Nejedlo, "Tribute^{TM} Board and Platform Test Methodology", Institute of Electrical and Electronics Engineers, Proceedings International Test Conference 2003, Charlotte, NC, USA Sept. 30 - Oct. 2, 2003 (IEEE, US Vol. 2, 30 September 2003) XP010685399, ISBN: 0-7803-8106-8 (pages 106-113)		

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